

Sheet 1 of 1

Based on Form PTO-1449 (3/90)		ATTY. DOCKET NO. 450100-05079	SERIAL NO. 1521054 Filed Concurrently Herewith
LIST OF REFERENCES CITED BY APPLICANT (Use several sheets if necessary)		APPLICANT Takashi YOKOKAWA et al.	
		FILING DATE Filed Concurrently Herewith	GROUP

U.S. PATENT DOCUMENTS							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
SWL	AA	6,539,367	03/25/03	Blanksby et al.			
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						

FOREIGN PATENT DOCUMENTS							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES NO
SWL	AL	JP 2003-269302	09/25/03	Japan			
SWL	AM	JP 2002-33670	01/31/02	Japan			
SWL	AN	EP 1 158 682	11/28/01	Europe			
	AO						
	AP						

OTHER PRIOR ART (Including Author, Title, Date, Pertinent Pages, Etc.)							
SWL	AQ		S. Kim et al, 'Parallel VSLI Architectures for a Class of LD PC Codes', IEEE International Symposium on Circuits and Systems, 2002, ISCAS 2002, Volume 2, November 2002, p. 26-29				
	AR						
	AS						

EXAMINER	DATE CONSIDERED
<i>R. B. n</i>	4/6/06

* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.